

FIG. 1 a

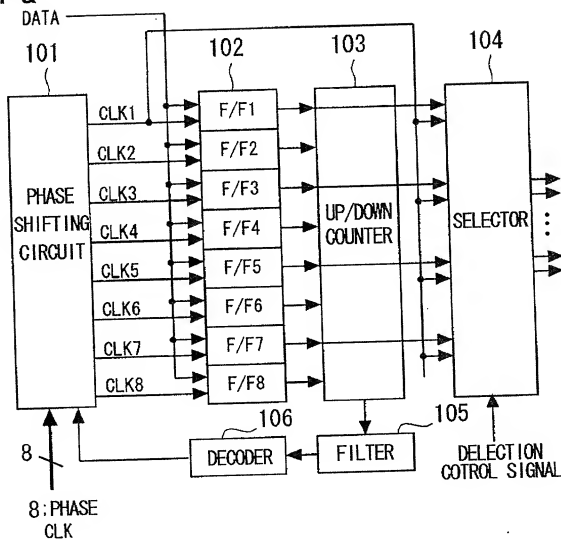


FIG. 1 b

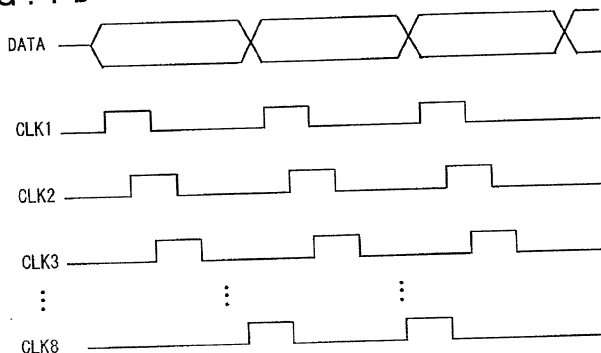


FIG. 2

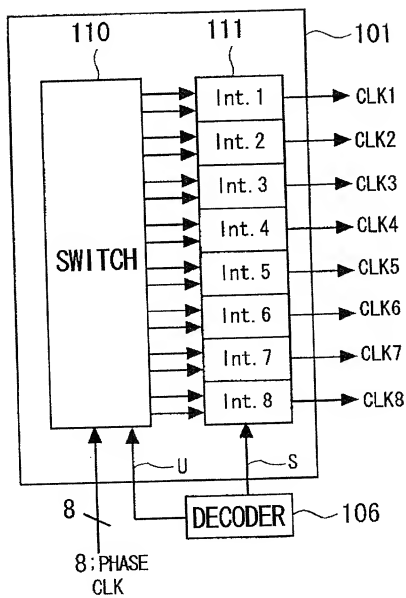


FIG. 3

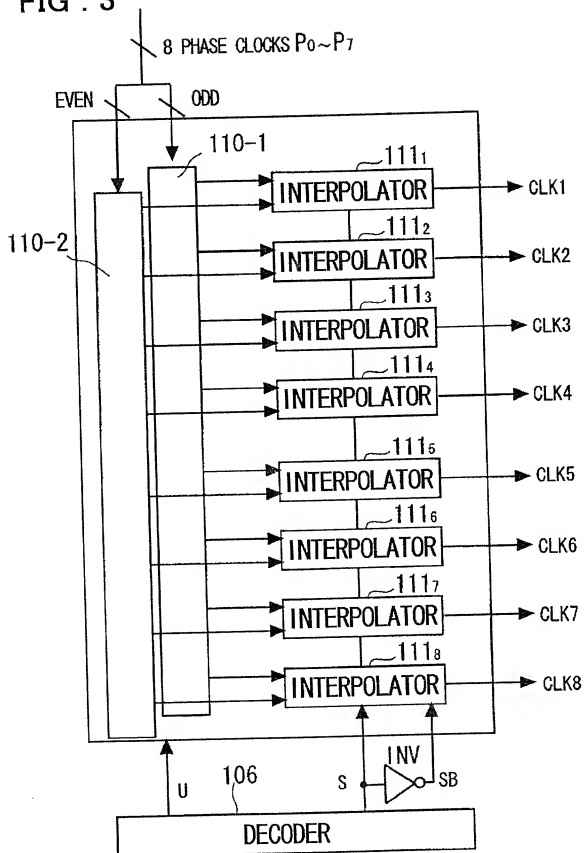


FIG . 4

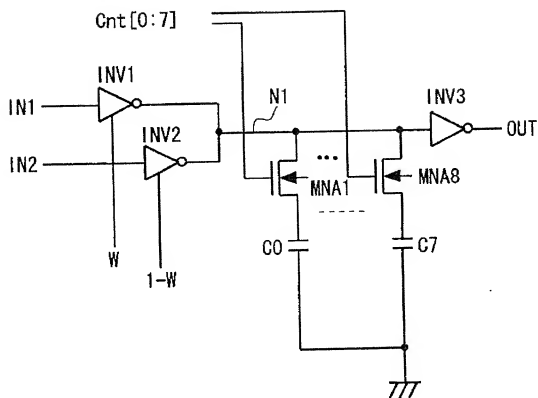


FIG . 5

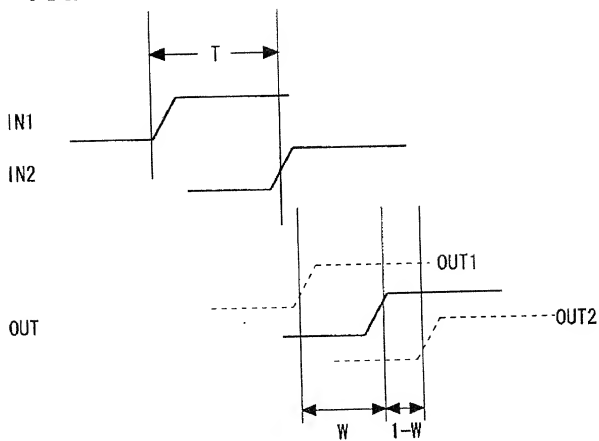




FIG. 7

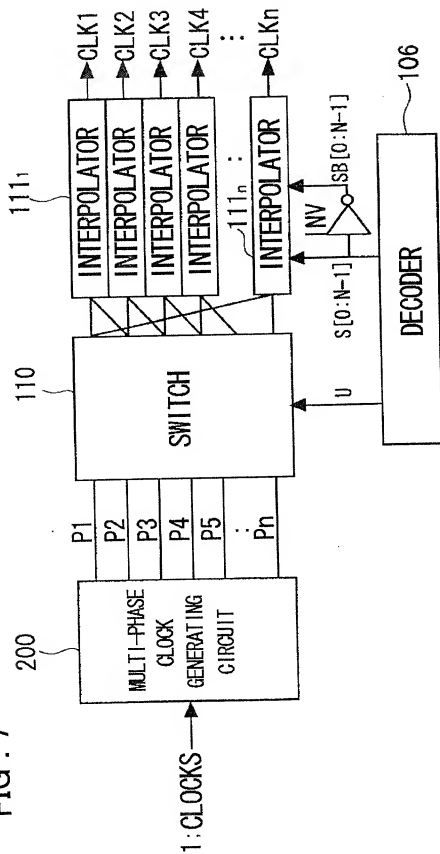


FIG. 8

205: CLOCK

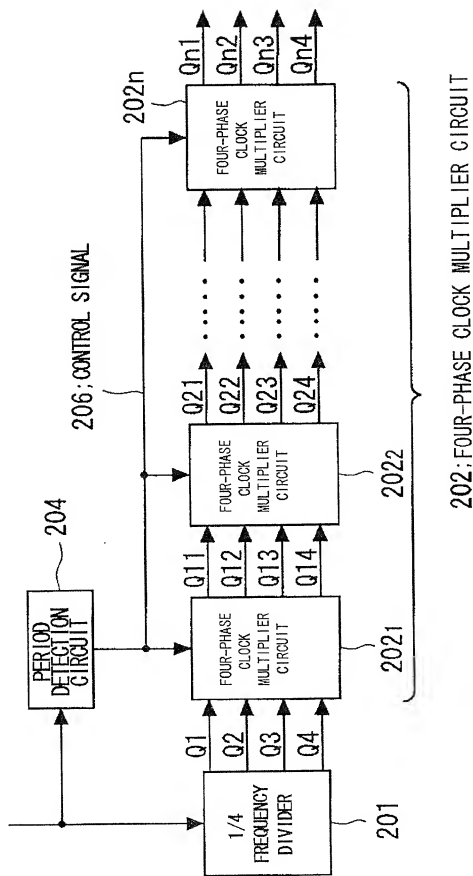




FIG. 9 a

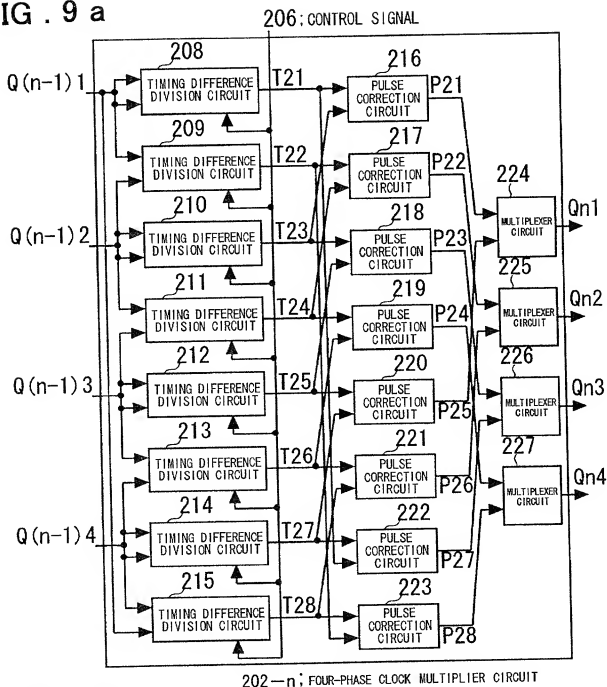
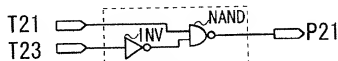
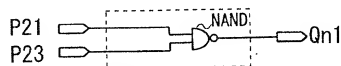


FIG. 9 b



PULSE CORRECTION CIRCUIT

FIG. 9 c



MULTIPLEXER CIRCUIT

FIG. 10

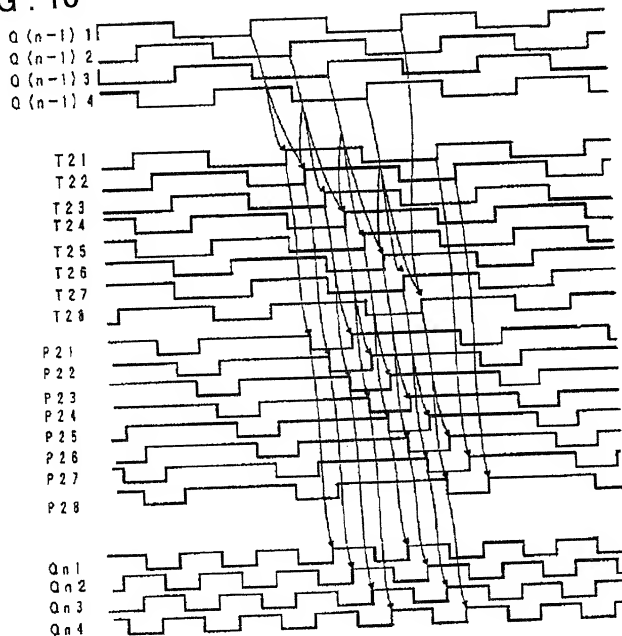


FIG . 11 a

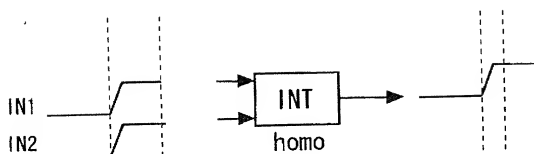


FIG . 11 b

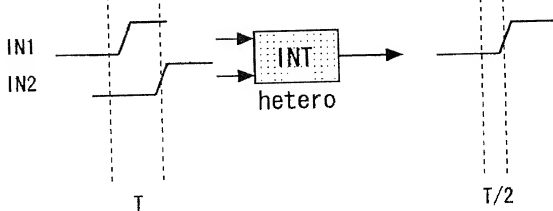
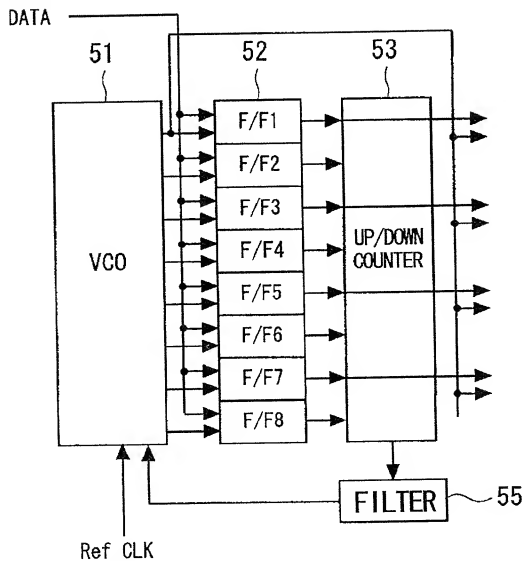




FIG. 13 PRIOR ART



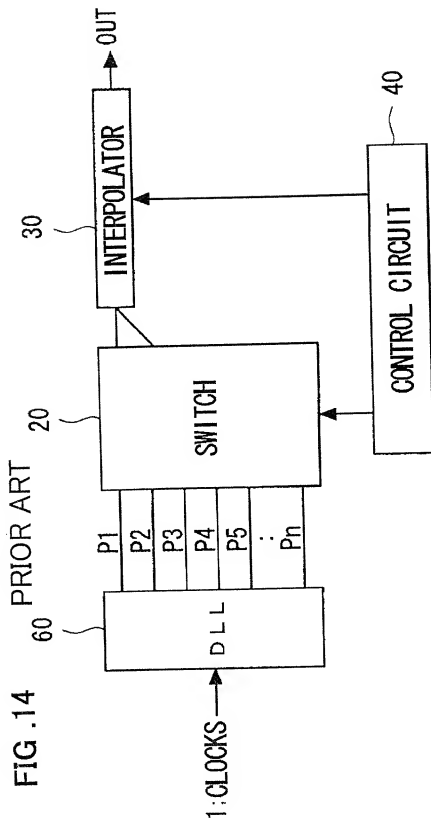


FIG. 15 PRIOR ART

